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Search Results - Record(s) 1 through 20 of 44 returned.

☐ 1. Document ID: US 6622181 B1

L7: Entry 1 of 44

File: USPT

Sep 16, 2003

US-PAT-NO: 6622181

DOCUMENT-IDENTIFIER: US 6622181 B1

TITLE: Timing window elimination in self-modifying direct memory access processors

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 2. Document ID: US 6553036 B1

L7: Entry 2 of 44

File: USPT

Apr 22, 2003

US-PAT-NO: 6553036

DOCUMENT-IDENTIFIER: US 6553036 B1

TITLE: Method and apparatus for preserving loop fairness with dynamic half-duplex

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 3. Document ID: US 6545981 B1

L7: Entry 3 of 44

File: USPT

Apr 8, 2003

US-PAT-NO: 6545981

DOCUMENT-IDENTIFIER: US 6545981 B1

TITLE: System and method for implementing error detection and recovery in a system area network

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 4. Document ID: US 6496740 B1

L7: Entry 4 of 44

File: USPT

Dec 17, 2002

US-PAT-NO: 6496740

DOCUMENT-IDENTIFIER: US 6496740 B1

TITLE: Transfer controller with hub and ports architecture

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 5. Document ID: US 6493343 B1

L7: Entry 5 of 44

File: USPT

Dec 10, 2002

US-PAT-NO: 6493343

DOCUMENT-IDENTIFIER: US 6493343 B1

TITLE: System and method for implementing multi-pathing data transfers in a system area network

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 6. Document ID: US 6487617 B1

L7: Entry 6 of 44

File: USPT

Nov 26, 2002

US-PAT-NO: 6487617

DOCUMENT-IDENTIFIER: US 6487617 B1

TITLE: Source-destination re-timed cooperative communication bus

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 7. Document ID: US 6477174 B1

L7: Entry 7 of 44

File: USPT

Nov 5, 2002

US-PAT-NO: 6477174

DOCUMENT-IDENTIFIER: US 6477174 B1

TITLE: Polling response selection using request monitoring in a network switch apparatus

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 8. Document ID: US 6473793 B1

L7: Entry 8 of 44

File: USPT

Oct 29, 2002

US-PAT-NO: 6473793

DOCUMENT-IDENTIFIER: US 6473793 B1

TITLE: Method and apparatus for selectively allocating and enforcing bandwidth usage requirements on network users

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 9. Document ID: US 6400819 B1

L7: Entry 9 of 44

File: USPT

Jun 4, 2002

US-PAT-NO: 6400819

DOCUMENT-IDENTIFIER: US 6400819 B1

TITLE: Method and apparatus for executing communication in real-time and data structure for real-time data communication

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 10. Document ID: US 6347344 B1

L7: Entry 10 of 44

File: USPT

Feb 12, 2002

US-PAT-NO: 6347344

DOCUMENT-IDENTIFIER: US 6347344 B1

TITLE: Integrated multimedia system with local processor, data transfer switch, processing modules, fixed functional unit, data streamer, interface unit and multiplexer, all integrated on multimedia processor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 11. Document ID: US 6336179 B1

L7: Entry 11 of 44

File: USPT

Jan 1, 2002

US-PAT-NO: 6336179

DOCUMENT-IDENTIFIER: US 6336179 B1

TITLE: Dynamic scheduling mechanism for an asynchronous/isochronous integrated circuit interconnect bus

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 12. Document ID: US 6260088 B1

L7: Entry 12 of 44

File: USPT

Jul 10, 2001

US-PAT-NO: 6260088

DOCUMENT-IDENTIFIER: US 6260088 B1

**** See image for Certificate of Correction ****

TITLE: Single integrated circuit embodying a risc processor and a digital signal processor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 13. Document ID: US 6195331 B1

L7: Entry 13 of 44

File: USPT

Feb 27, 2001

US-PAT-NO: 6195331

DOCUMENT-IDENTIFIER: US 6195331 B1

TITLE: Method of managing transmission buffer memory and ATM communication device
using the method

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 14. Document ID: US 6157971 A

L7: Entry 14 of 44

File: USPT

Dec 5, 2000

US-PAT-NO: 6157971

DOCUMENT-IDENTIFIER: US 6157971 A

TITLE: Source-destination re-timed cooperative communication bus

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 15. Document ID: US 6081860 A

L7: Entry 15 of 44

File: USPT

Jun 27, 2000

US-PAT-NO: 6081860

DOCUMENT-IDENTIFIER: US 6081860 A

TITLE: Address pipelining for data transfers

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 16. Document ID: US 6038584 A

L7: Entry 16 of 44

File: USPT

Mar 14, 2000

US-PAT-NO: 6038584

DOCUMENT-IDENTIFIER: US 6038584 A

TITLE: Synchronized MIMD multi-processing system and method of operation

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 17. Document ID: US 6011791 A

L7: Entry 17 of 44

File: USPT

Jan 4, 2000

US-PAT-NO: 6011791

DOCUMENT-IDENTIFIER: US 6011791 A

TITLE: Multi-processor system and its network

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw De
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☐ 18. Document ID: US 5974045 A

L7: Entry 18 of 44

File: USPT

Oct 26, 1999

US-PAT-NO: 5974045

DOCUMENT-IDENTIFIER: US 5974045 A

TITLE: OAM processing device in an ATM network

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw De
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☐ 19. Document ID: US 5933624 A

L7: Entry 19 of 44

File: USPT

Aug 3, 1999

US-PAT-NO: 5933624

DOCUMENT-IDENTIFIER: US 5933624 A

TITLE: Synchronized MIMD multi-processing system and method inhibiting instruction fetch at other processors while one processor services an interrupt

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw De
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☐ 20. Document ID: US 5881272 A

L7: Entry 20 of 44

File: USPT

Mar 9, 1999

US-PAT-NO: 5881272

DOCUMENT-IDENTIFIER: US 5881272 A

TITLE: Synchronized MIMD multi-processing system and method inhibiting instruction fetch at other processors on write to program counter of one processor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw De
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Term	Documents
NUMBER	1772192
NUMBERS	411799
DATA	814419
DATUM	14565
TRANSFER\$5	0
TRANSFER	615961
TRANSFERA	6
TRANSFERAASE	2
TRANSFERABALE	1
TRANSFERABEL	1
TRANSFERABIE	1
(L6 AND TRANSFER\$5 NEAR6 REQUEST\$3 NEAR5 (COUNT\$3 OR NUMBER) NEAR5 (PACKET\$3 OR DATA OR WORD\$1 OR BYTE\$1)).USPT.	44

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Search Results - Record(s) 21 through 40 of 44 returned.

☐ 21. Document ID: US 5881065 A

L7: Entry 21 of 44

File: USPT

Mar 9, 1999

US-PAT-NO: 5881065

DOCUMENT-IDENTIFIER: US 5881065 A

** See image for Certificate of Correction **

TITLE: Data transfer switch for transferring data of an arbitrary length on the basis of transfer destination

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 22. Document ID: US 5867661 A

L7: Entry 22 of 44

File: USPT

Feb 2, 1999

US-PAT-NO: 5867661

DOCUMENT-IDENTIFIER: US 5867661 A

TITLE: Method and apparatus of using virtual sockets for reducing data transmitted over a wireless communication link between a client web browser and a host web server using a standard TCP protocol

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 23. Document ID: US 5862206 A

L7: Entry 23 of 44

File: USPT

Jan 19, 1999

US-PAT-NO: 5862206

DOCUMENT-IDENTIFIER: US 5862206 A

TITLE: Method and apparatus for performing raw cell status report frequency mitigation on transmit in a network node

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 24. Document ID: US 5835967 A

L7: Entry 24 of 44

File: USPT

Nov 10, 1998

h e b b c g b c c e

US-PAT-NO: 5835967
DOCUMENT-IDENTIFIER: US 5835967 A

TITLE: Adjusting prefetch size based on source of prefetch address

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 25. Document ID: US 5809288 A

L7: Entry 25 of 44

File: USPT

Sep 15, 1998

US-PAT-NO: 5809288
DOCUMENT-IDENTIFIER: US 5809288 A

TITLE: Synchronized MIMD multi-processing system and method inhibiting instruction fetch on memory access stall

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 26. Document ID: US 5802569 A

L7: Entry 26 of 44

File: USPT

Sep 1, 1998

US-PAT-NO: 5802569
DOCUMENT-IDENTIFIER: US 5802569 A

TITLE: Computer system having cache prefetching amount based on CPU request types

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 27. Document ID: US 5724600 A

L7: Entry 27 of 44

File: USPT

Mar 3, 1998

US-PAT-NO: 5724600
DOCUMENT-IDENTIFIER: US 5724600 A

TITLE: Parallel processor system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 28. Document ID: US 5724599 A

L7: Entry 28 of 44

File: USPT

Mar 3, 1998

US-PAT-NO: 5724599
DOCUMENT-IDENTIFIER: US 5724599 A

TITLE: Message passing and blast interrupt from processor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw De
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☐ 29. Document ID: US 5664117 A

L7: Entry 29 of 44

File: USPT

Sep 2, 1997

US-PAT-NO: 5664117

DOCUMENT-IDENTIFIER: US 5664117 A

TITLE: Apparatus and method for prefetching data to load buffers in a bridge between two buses in a computer

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw De
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☐ 30. Document ID: US 5555542 A

L7: Entry 30 of 44

File: USPT

Sep 10, 1996

US-PAT-NO: 5555542

DOCUMENT-IDENTIFIER: US 5555542 A

**** See image for Certificate of Correction ****

TITLE: Packet directional path identifier transfer system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw De
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☐ 31. Document ID: US 5524265 A

L7: Entry 31 of 44

File: USPT

Jun 4, 1996

US-PAT-NO: 5524265

DOCUMENT-IDENTIFIER: US 5524265 A

TITLE: Architecture of transfer processor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw De
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☐ 32. Document ID: US 5448558 A

L7: Entry 32 of 44

File: USPT

Sep 5, 1995

US-PAT-NO: 5448558

DOCUMENT-IDENTIFIER: US 5448558 A

TITLE: Method and apparatus for managing packet FIFOs

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw De
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☐ 33. Document ID: US 5412651 A

L7: Entry 33 of 44

File: USPT

May 2, 1995

US-PAT-NO: 5412651

DOCUMENT-IDENTIFIER: US 5412651 A

TITLE: Structure and method for combining PCM and common control data on a backplane bus

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMNC	Drawn De
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☐ 34. Document ID: US 5301322 A

L7: Entry 34 of 44

File: USPT

Apr 5, 1994

US-PAT-NO: 5301322

DOCUMENT-IDENTIFIER: US 5301322 A

TITLE: System for converting job/process identifiers into processor/process identifiers in transferring data between processes in a multiprocessor system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMNC	Drawn De
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☐ 35. Document ID: US 5291192 A

L7: Entry 35 of 44

File: USPT

Mar 1, 1994

US-PAT-NO: 5291192

DOCUMENT-IDENTIFIER: US 5291192 A

TITLE: Paging system and its operating method with repeated transmission

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMNC	Drawn De
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☐ 36. Document ID: US 5218602 A

L7: Entry 36 of 44

File: USPT

Jun 8, 1993

US-PAT-NO: 5218602

DOCUMENT-IDENTIFIER: US 5218602 A

**** See image for Certificate of Correction ****

TITLE: Interprocessor switching network

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMNC	Drawn De
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☐ 37. Document ID: US 5212777 A

L7: Entry 37 of 44

File: USPT

May 18, 1993

US-PAT-NO: 5212777

DOCUMENT-IDENTIFIER: US 5212777 A

TITLE: Multi-processor reconfigurable in single instruction multiple data (SIMD) and multiple instruction multiple data (MIMD) modes and method of operation

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 38. Document ID: US 5210751 A

L7: Entry 38 of 44

File: USPT

May 11, 1993

US-PAT-NO: 5210751

DOCUMENT-IDENTIFIER: US 5210751 A

TITLE: Signal transmission system capable of performing re-transmission control in units of slots

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 39. Document ID: US 5140582 A

L7: Entry 39 of 44

File: USPT

Aug 18, 1992

US-PAT-NO: 5140582

DOCUMENT-IDENTIFIER: US 5140582 A

**** See image for Certificate of Correction ****

TITLE: Packet switching system having bus matrix switch

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 40. Document ID: US 5010329 A

L7: Entry 40 of 44

File: USPT

Apr 23, 1991

US-PAT-NO: 5010329

DOCUMENT-IDENTIFIER: US 5010329 A

**** See image for Certificate of Correction ****

TITLE: Block polling data communication system having optimum block determination means

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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Clear	Generate Collection	Print	Fwd Refs	Bkwd Refs	Generate OACS
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Term	Documents
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NUMBER	1772192
NUMBERS	411799
DATA	814419
DATUM	14565
TRANSFER\$5	0
TRANSFER	615961
TRANSFERA	6
TRANSFERAASE	2
TRANSFERABALE	1
TRANSFERABEL	1
TRANSFERABIE	1
(L6 AND TRANSFER\$5 NEAR6 REQUEST\$3 NEAR5 (COUNT\$3 OR NUMBER) NEAR5 (PACKET\$3 OR DATA OR WORD\$1 OR BYTE\$1)).USPT.	44

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DOCUMENT-IDENTIFIER: US 5539916 A

TITLE: DMA control for continuing transfer to input/output device in a cycle steal mode

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims RWMC Drawn Da

Clear Generate Collection Print Fwd Refs Bkwd Refs Generate OAGS

Term	Documents
PULSE\$1	0
PULSE	333008
PULSEA	5
PULSEB	4
PULSEC	1
PULSED	65838
PULSEE	10
PULSEH	1
PULSEI	1
PULSEL	6
PULSEN	12
(L19 AND (PULSE\$1 OR CLOCK\$1) NEAR5 REQUEST\$3 AND L11).USPT.	37

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PULSE	333008
PULSEA	5
PULSEB	4
PULSEC	1
PULSED	65838
PULSEE	10
PULSEH	1
PULSEI	1
PULSEL	6
PULSEN	12
(L19 AND (PULSE\$1 OR CLOCK\$1) NEAR5 REQUEST\$3 AND L11).USPT.	37

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 IBM Technical Disclosure Bulletins

Search:

request\$3 near7 (number or count\$3)

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Recall Text

Clear

Interrupt

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Set
Name Query
 side by
 side

Hit
Count Set
 Name
 result
 set

DB=USPT; PLUR=YES; OP=OR

<u>L21</u>	L19 and (pulse\$1 or clock\$1) near5 request\$3 and l11	37	<u>L21</u>
<u>L20</u>	L19 and (pulse\$1 or clock\$1) near5 request\$3	161	<u>L20</u>
<u>L19</u>	request\$1 near8 (incom\$5 ot outgoing or input\$4 or output\$4) near15 (number or count\$3) near5 (data or word\$1 or byte\$1 or packet\$1)	940	<u>L19</u>
<u>L18</u>	L17 and (cop\$4 or duplicat\$3) near4 (data or packet\$1)	31	<u>L18</u>
<u>L17</u>	data near5 transfer\$5 near5 request\$1 near8 (number\$1 or count\$3) and packet\$3 near5 (hold\$3 or held or fifo or buffer\$1)	58	<u>L17</u>
<u>L16</u>	L15 and (auto or self) near3 synchron\$7	0	<u>L16</u>
<u>L15</u>	l11 and L14	76	<u>L15</u>
<u>L14</u>	data near5 transfer\$5 near5 request\$1 near8 (number\$1 or count\$3) and packet\$3	152	<u>L14</u>
<u>L13</u>	data near5 transfer\$5 near5 request\$1 near8 (number\$1 or count\$3)	510	<u>L13</u>
<u>L12</u>	l4 and L11	23	<u>L12</u>
<u>L11</u>	l7 or l8 or l9 or L10	22343	<u>L11</u>
<u>L10</u>	(((709/230-253)))[CCLS]	5756	<u>L10</u>
<u>L9</u>	(((712/28-228)))[CCLS]	5777	<u>L9</u>
<i>DB=PGPB; PLUR=YES; OP=OR</i>			
<u>L8</u>	(((712/28-228)))[CCLS]	757	<u>L8</u>
<i>DB=USPT; PLUR=YES; OP=OR</i>			
<u>L7</u>	(((710/20-244)))[CCLS]	12267	<u>L7</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L6</u>	((710/20-244))!	0	<u>L6</u>
<u>L5</u>	(712/20-244)!	0	<u>L5</u>
<u>L4</u>	L2 and (number\$1 or count\$3) near4 data near9 request\$3	70	<u>L4</u>
<u>L3</u>	L2 and (number\$1 or count\$3) near4 data	175	<u>L3</u>
<u>L2</u>	data near5 transfer\$5 near5 request\$1 near8 (pulse\$1 or clock\$1)	313	<u>L2</u>
<u>L1</u>	data near5 transfer\$5 near5 request\$1 near8 (pulse\$1 oe clock\$1)	920	<u>L1</u>

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☐ 41. Document ID: US 4972314 A

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File: USPT

Nov 20, 1990

US-PAT-NO: 4972314

DOCUMENT-IDENTIFIER: US 4972314 A

TITLE: Data flow signal processor method and apparatus

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 42. Document ID: US 4827473 A

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File: USPT

May 2, 1989

US-PAT-NO: 4827473

DOCUMENT-IDENTIFIER: US 4827473 A

TITLE: Packet switching system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 43. Document ID: US 4692917 A

L7: Entry 43 of 44

File: USPT

Sep 8, 1987

US-PAT-NO: 4692917

DOCUMENT-IDENTIFIER: US 4692917 A

TITLE: Packet switching system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 44. Document ID: US 3629853 A

L7: Entry 44 of 44

File: USPT

Dec 21, 1971

US-PAT-NO: 3629853

DOCUMENT-IDENTIFIER: US 3629853 A

** See image for Certificate of Correction **

h e b b c g b c c e

TITLE: DATA-PROCESSING ELEMENT

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw De
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Clear	Generate Collection	Print	Fwd Refs	Bkwd Refs	Generate OACS
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Term	Documents
NUMBER	1772192
NUMBERS	411799
DATA	814419
DATUM	14565
TRANSFER\$5	0
TRANSFER	615961
TRANSFERA	6
TRANSFERAASE	2
TRANSFERABALE	1
TRANSFERABEL	1
TRANSFERABIE	1
(L6 AND TRANSFER\$5 NEAR6 REQUEST\$3 NEAR5 (COUNT\$3 OR NUMBER) NEAR5 (PACKET\$3 OR DATA OR WORD\$1 OR BYTE\$1)).USPT.	44

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